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-	1265	(variable change\$4 flexible shifting adapt\$4 alter\$4 adjust\$4 switch\$4) with (voltage adj generat\$3) and (sense adj amp\$6)	USPAT; US-PGPUB; EPO; JPO; IBM_TDB	2003/12/18 10:40
-	110022	reference adj voltage	USPAT; US-PGPUB; EPO; JPO; IBM_TDB	2003/12/18 09:47
-	1853	(loop adj back loopback) with (generat\$3 compar\$3)	USPAT; US-PGPUB; EPO; JPO; IBM_TDB	2003/12/18 09:48
-	81043	DAC D/A adj convert\$3 digital-to-analog adj convert\$3	USPAT; US-PGPUB; EPO; JPO; IBM_TDB	2003/12/18 09:55
-	29832	compar\$4 same reference same (analog direct adj current DC) same (volt\$3 power level\$3)	USPAT; US-PGPUB; EPO; JPO; IBM_TDB	2003/12/18 10:00

*Threshold same voltage*  
*integrated circuit*  
*amps*  
*Compane*  
*DC*  
*324 / \$*

-	2300	boundary-scan boundary adj scan	USPAT; US-PGPUB; EPO; JPO; IBM_TDB	2003/12/18 10:02
-	1	((variable change\$4 flexible shifting adapt\$4 alter\$4 adjust\$4 switch\$4) with (voltage adj generat\$3) and (sense adj amp\$6)) and (reference adj voltage) and ((loop adj back loopback) with (generat\$3 compar\$3)) and (DAC D/A adj convert\$3 digital-to-analog adj convert\$3 ) and (compar\$4 same reference same (analog direct adj current DC) same (volt\$3 power level\$3)) and (compar\$4 same reference same (analog direct adj current DC) same (volt\$3 power level\$3)) and (boundary-scan boundary adj scan)	USPAT; US-PGPUB; EPO; JPO; IBM_TDB	2003/12/18 10:11
-	1	((variable change\$4 flexible shifting adapt\$4 alter\$4 adjust\$4 switch\$4) with (voltage adj generat\$3) and (sense adj amp\$6)) and (reference adj voltage) and ((loop adj back loopback) with (generat\$3 compar\$3)) and (DAC D/A adj convert\$3 digital-to-analog adj convert\$3 ) and (compar\$4 same reference same (analog direct adj current DC) same (volt\$3 power level\$3)) and (compar\$4 same reference same (analog direct adj current DC) same (volt\$3 power level\$3))	USPAT; US-PGPUB; EPO; JPO; IBM_TDB	2003/12/18 10:11
-	1	((variable change\$4 flexible shifting adapt\$4 alter\$4 adjust\$4 switch\$4) with (voltage adj generat\$3) and (sense adj amp\$6)) and (reference adj voltage) and ((loop adj back loopback) with (generat\$3 compar\$3)) and (DAC D/A adj convert\$3 digital-to-analog adj convert\$3 ) and (compar\$4 same reference same (analog direct adj current DC) same (volt\$3 power level\$3))	USPAT; US-PGPUB; EPO; JPO; IBM_TDB	2003/12/18 10:12
-	1	((variable change\$4 flexible shifting adapt\$4 alter\$4 adjust\$4 switch\$4) with (voltage adj generat\$3) and (sense adj amp\$6)) and (reference adj voltage) and ((loop adj back loopback) with (generat\$3 compar\$3)) and (DAC D/A adj convert\$3 digital-to-analog adj convert\$3 )	USPAT; US-PGPUB; EPO; JPO; IBM_TDB	2003/12/18 10:12
-	263109	(voltage adj generat\$3 source) and (sense adj amp\$6 receiver comparator)	USPAT; US-PGPUB; EPO; JPO; IBM_TDB	2003/12/18 10:42
-	47441	((voltage adj generat\$3 source) and (sense adj amp\$6 receiver comparator)) and (reference adj voltage) ((loop adj back loopback) with (generat\$3 compar\$3))	USPAT; US-PGPUB; EPO; JPO; IBM_TDB	2003/12/18 10:42
-	57	((voltage adj generat\$3 source) and (sense adj amp\$6 receiver comparator)) and (reference adj voltage) and ((loop adj back loopback) with (generat\$3 compar\$3))	USPAT; US-PGPUB; EPO; JPO; IBM_TDB	2003/12/18 10:50
-	1352	(324/522,523,527).ccls.	USPAT; US-PGPUB; EPO; JPO; IBM_TDB	2003/12/18 10:44
-	2360	(714/819,727,736,734,733).ccls.	USPAT; US-PGPUB; EPO; JPO; IBM_TDB	2003/12/18 10:45
-	518	(341/94).ccls.	USPAT; US-PGPUB; EPO; JPO; IBM_TDB	2003/12/18 10:45

-	0	((voltage adj generat\$3 source) and (sense adj amp\$6 receiver comparator)) and (reference adj voltage) and ((loop adj back loopback) with (generat\$3 compar\$3))) and ((324/522,523,527).ccls.) and ((714/819,727,736,734,733).ccls.) and ((341/94).ccls.)	USPAT; US-PGPUB; EPO; JPO; IBM_TDB	2003/12/18 10:46
-	0	((voltage adj generat\$3 source) and (sense adj amp\$6 receiver comparator)) and (reference adj voltage) and ((loop adj back loopback) with (generat\$3 compar\$3))) and ((324/522,523,527).ccls.)	USPAT; US-PGPUB; EPO; JPO; IBM_TDB	2003/12/18 10:46
-	72	(reference adj voltage) and ((loop adj back loopback) with (generat\$3 compar\$3))	USPAT; US-PGPUB; EPO; JPO; IBM_TDB	2003/12/18 10:48
-	7484	((reference adj voltage) and ((loop adj back loopback) with (generat\$3 compar\$3))) and ((324/522,523,527).ccls.) and ((714/819,727,736,734,733).ccls.) nad ((341/94).ccls.)	USPAT; US-PGPUB; EPO; JPO; IBM_TDB	2003/12/18 10:48
-	0	((reference adj voltage) and ((loop adj back loopback) with (generat\$3 compar\$3))) and ((324/522,523,527).ccls.) and ((714/819,727,736,734,733).ccls.) and ((341/94).ccls.)	USPAT; US-PGPUB; EPO; JPO; IBM_TDB	2003/12/18 10:49
-	1	((voltage adj generat\$3 source) and (sense adj amp\$6 receiver comparator)) and (reference adj voltage) ((loop adj back loopback) with (generat\$3 compar\$3))) and ((324/522,523,527).ccls.) and ((714/819,727,736,734,733).ccls.)	USPAT; US-PGPUB; EPO; JPO; IBM_TDB	2003/12/18 10:49
-	1		USPAT	2003/12/18 11:11
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... is constrained by the output swing of the fixed +2 closed-**loop** gain amplifier. ... The  
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